CERTIFICATE OF MAILING BY "EXPRESS MAIL" (37 CFR 1.10)			Docket No.	
Applicant(s): Greg Scott & al.		14993.18		
	3			
Application No.	OCT 1 2004	Examiner	Customer No.	Group Art Unit
10/611,7	July 1, 2003	Anh Quan Tra	022913	2816
Invention: DO N	BLE-SIDED EXTENDED DRA VOLATEGE AND REVERSI	AIN FIELD EFFECT TRANSISTOR, A E VOLTAGE PROTECTION CIRCUI	AND INTEGRATI T THAT USES T	ED HE SAME
I hereby certify	that the following corresponde	ence:		
Transmittal Le	tter (2 pages); Response to Res	triction Requirement (2 pages); Postca	rd	
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CFR 1.10 in an	envelope addressed to: Com	missioner for Patents, P.O. Box 1450,	, Alexandria, VA	22313-1450 on
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XPRESS MAIL LABEL NO.: EV525475200US

PATENT APPLICATION
Docket No: 14993.18

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of		)
	Greg Scott et al.	)
Serial No.:	10/611,714	) ) Art Uni
Filed:	July 1, 2003	) 2816
Conf No.:	4464	)
For:	DOUBLE-SIDED EXTENDED DRAIN FIELD EFFECT TRANSISTOR, AND INTEGRATED OVERVOLTAGE AND REVERSE VOLTAGE PROTECTION CIRCUIT THAT USES THE SAME	) ) ) )
Examiner:	Anh Quan Tra	)

## TRANSMITTAL LETTER

Mail Stop AMENDMENT Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

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Transmitted herewith is a Response to Restriction Requirement for entry in the above-identified application.

- X The Commissioner is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to Deposit Account No. 23-3178. Triplicate copies of this sheet are attached.
  - X Any filing fees under 37 CFR 1.16 for the presentation of extra claims.

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X Any patent application processing fees under 37 CFR 1.17.

Dated this 1<sup>st</sup> day of October, 2004.

Respectfully submitted,

Adrian J. Lee

Attorney for Applicant Registration No. 42,785

Customer No. 022913

AJL:ds DS0000002577V001

PATENT APPLICATION
Docket No. 14993.18

EXPRESS MAIL LABEL NO.: EV 525475200 US

PATENT AID DOCKET NO.: PATENT AID TRADEMARK OFFICE

In re application of Greg Scott et al. 10/611,714 )Art Unit Serial No.: 2816 Filed: July 1, 2003 Conf No.: 4464 For: DOUBLE-SIDED EXTENDED DRAIN FIELD EFFECT TRANSISTOR, AND INTEGRATED OVERVOLTAGE AND REVERSE VOLTAGE PROTECTION CIRCUIT THAT USES THE SAME Examiner: Anh Quan Tra

## RESPONSE TO RESTRICTION REQUIREMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In response to the restriction requirement dated September 16, 2004, in the above-identified application. Applicant(s) hereby elect claims 1-20.

In the event the Examiner has any questions or comments concerning this communication, the Examiner is respectfully requested to initiate a telephonic interview with the undersigned attorney.

Dated this 1<sup>st</sup> day of October, 2004.

Respectfully submitted,

Adrian J. Lee

Attorney for Applicant Registration No. 42,785 Customer No. 022913

AJL:ds DS0000002581V001